

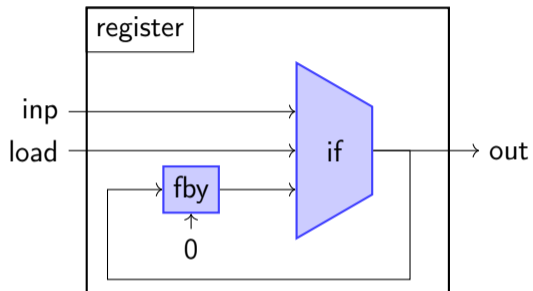
Towards Control Structures in Velus

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under the supervision of Timothy Bourke and Marc Pouzet

Inria - PARKAS

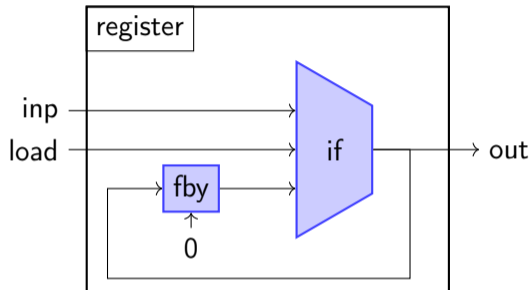
Synchron 2021 - 23 Nov

The Lustre Programming Language



inp	3	4	1	5	2	6	1	1	...
load	F	F	T	F	T	T	F	T	...
out	0	0	1	1	2	6	6	1	...

The Lustre Programming Language




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load	F	F	T	F	T	T	F	T	...
out	0	0	1	1	2	6	6	1	...

```
node register(inp : int; load : bool);
returns out : int;
let out = if load then inp else (0 fby load);
tel;
```

Stream Semantics of Lustre

inp	3	4	1	5	2	6	1	1	...
load	F	F	T	F	T	T	F	T	...
out	0	0	1	1	2	6	6	1	...

```
every trigger {  
  read inputs;  
  calculate;  
  write outputs;  
}
```



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$$\frac{H(x) = vs}{G, H, bs \vdash x \Downarrow vs}$$

Inductive `sem_exp`:

| Svar: `sem_var H x vs` \rightarrow
`sem_exp G H bs (Evar x ann) [vs] [...]`


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with sem_equation:


| Seq: Forall2 (sem_exp H) es ss \rightarrow
 Forall2 (sem_var H) xs (concat ss) \rightarrow
 sem_equation G H bs (xs, es)

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$$\frac{\text{node}(G, f) \doteq n \quad H(n.\text{in}) = xs \quad H(n.\text{out}) = ys \quad \forall eq \in n.\text{eqs}, G, H, (\text{base-of } xs) \vdash eq}{G \vdash f(xs) \Downarrow ys}$$

Coinductive semantics of the if operator

$$\frac{\text{ite } cs \ ts \ fs \doteq vs}{\text{ite } (\langle T \rangle \cdot cs) \ (\langle t \rangle \cdot ts) \ (\langle f \rangle \cdot fs) \doteq \langle t \rangle \cdot vs}$$

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Coinductive semantics of the if operator

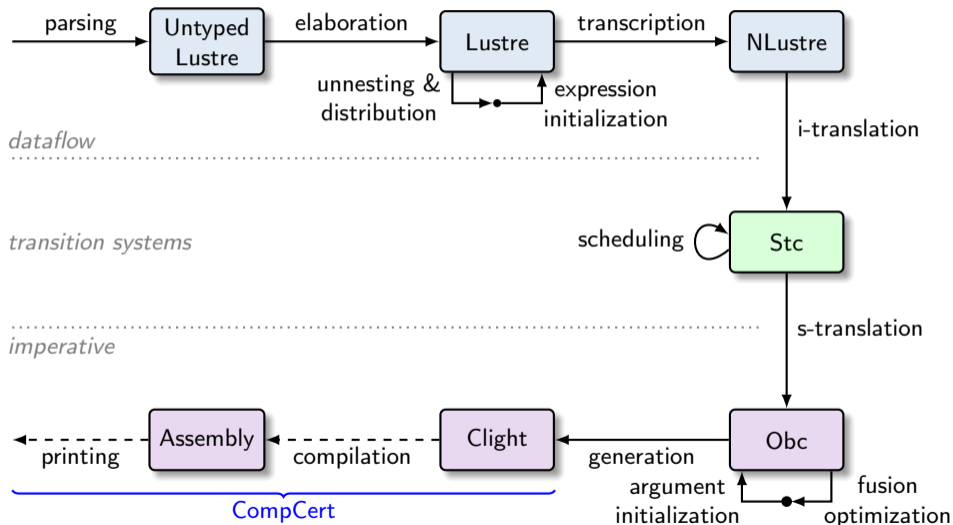
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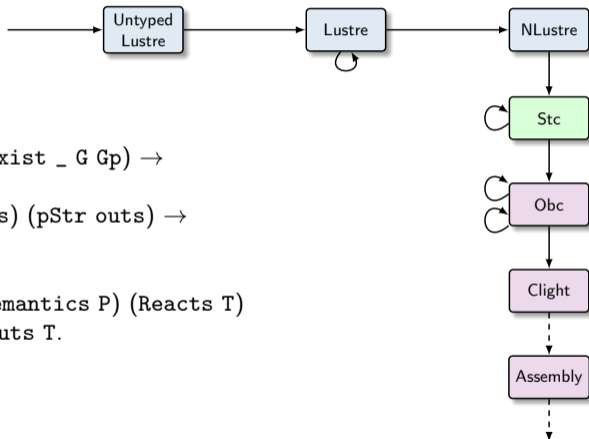
$$\frac{\text{ite } cs \ ts \ fs \doteq vs}{\text{ite } (\langle \rangle \cdot cs) \ (\langle \rangle \cdot ts) \ (\langle \rangle \cdot fs) \doteq \langle \rangle \cdot vs}$$

$$\frac{G, H, bs \vdash e \Downarrow [s] \quad G, H, bs \vdash \mathbf{e}_t \Downarrow ts \quad G, H, bs \vdash \mathbf{e}_f \Downarrow fs \quad \text{ite } s \ ts \ fs \doteq vs}{G, H, bs \vdash \text{if } e \text{ then } \mathbf{e}_t \text{ else } \mathbf{e}_f \Downarrow vs}$$

The Velus Compiler



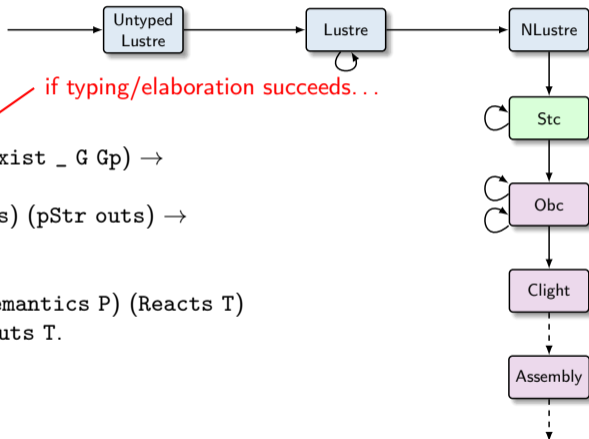
Main theorem



Theorem `behavior_asm`:

$$\begin{aligned} &\forall D \ G \ Gp \ P \ \text{main} \ \text{ins} \ \text{outs}, \\ &\text{elab_declarations } D = \text{OK} \ (\text{exist } _ \ G \ Gp) \rightarrow \\ &\text{compile } D \ \text{main} = \text{OK} \ P \rightarrow \\ &\text{Sem.sem_node } G \ \text{main} \ (\text{pStr } \text{ins}) \ (\text{pStr } \text{outs}) \rightarrow \\ &\text{wt_ins } G \ \text{main} \ \text{ins} \rightarrow \\ &\text{wc_ins } G \ \text{main} \ \text{ins} \rightarrow \\ &\exists T, \ \text{program_behaves} \ (\text{Asm.semantics } P) \ (\text{Reacts } T) \\ &\quad \wedge \ \text{bisim_IO } G \ \text{main} \ \text{ins} \ \text{outs} \ T. \end{aligned}$$

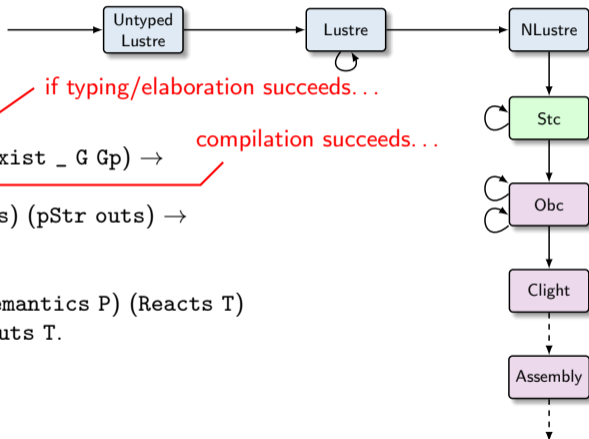
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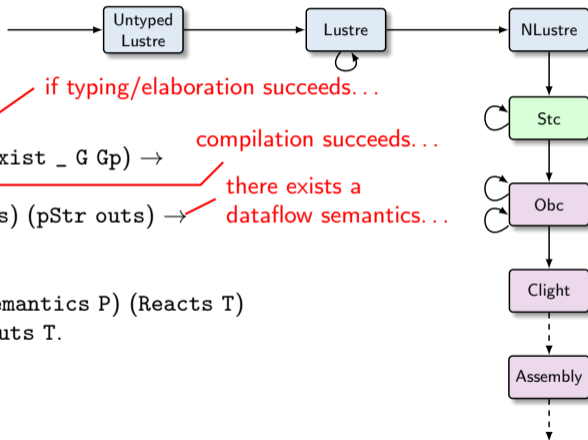
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if typing/elaboration succeeds...

compilation succeeds...

Main theorem

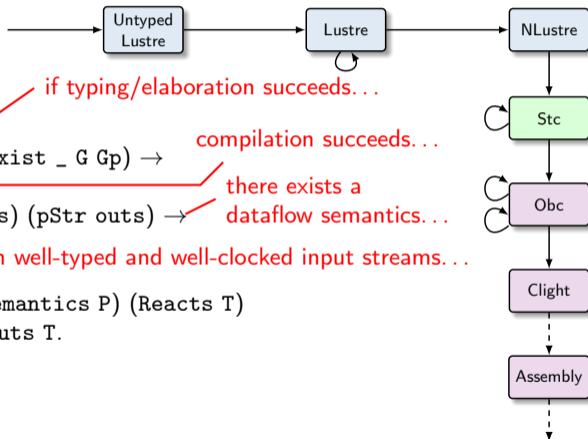


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if typing/elaboration succeeds...
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there exists a dataflow semantics...

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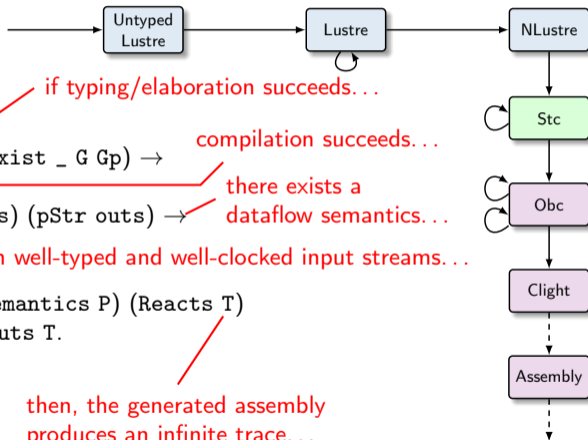


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if typing/elaboration succeeds...
compilation succeeds...
there exists a dataflow semantics...
with well-typed and well-clocked input streams...

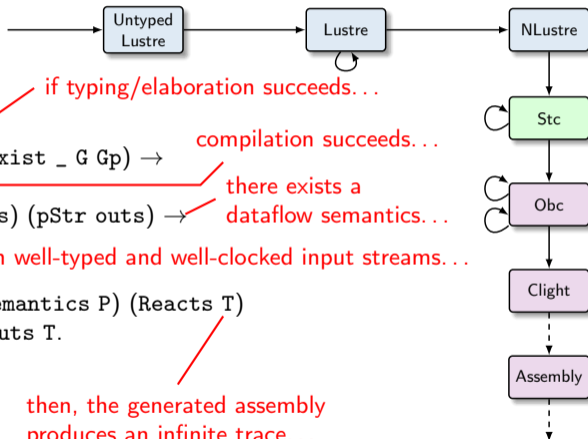
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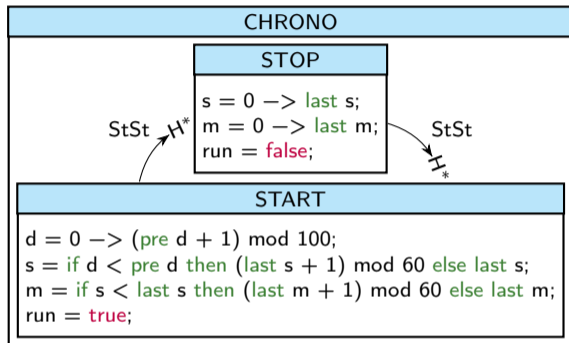


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if typing/elaboration succeeds...
compilation succeeds...
there exists a dataflow semantics...
with well-typed and well-clocked input streams...
then, the generated assembly produces an infinite trace...
... that corresponds to the dataflow model.

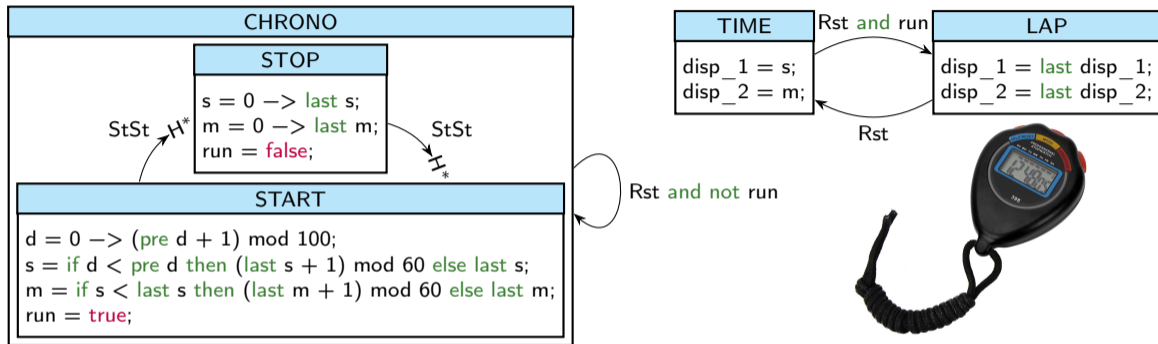
Extending Velus with control structures



Rst and not run



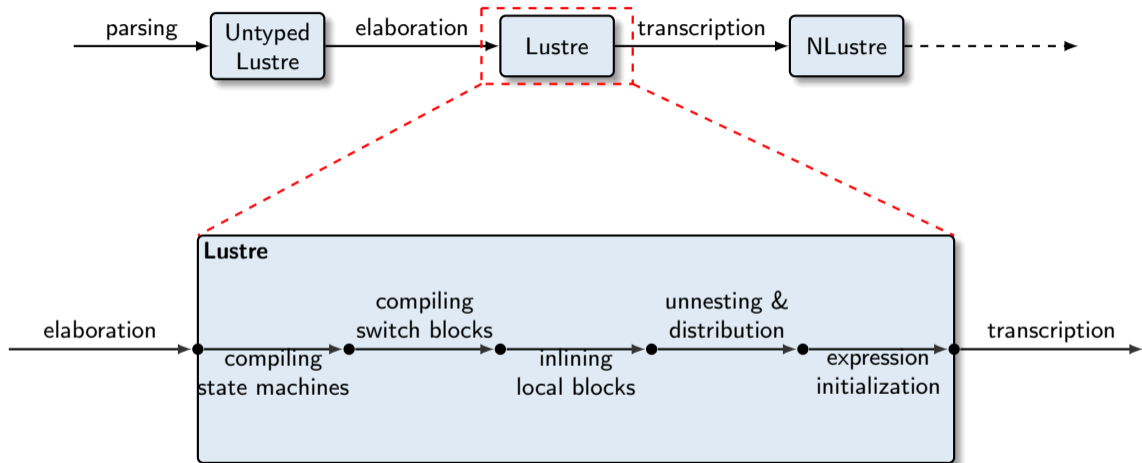
Extending Velus with control structures



Intermediate structures used to compile state machines:

- Switch blocks
- Reset blocks
- Local blocks (useful for compiling other constructs)

Extending the Velus compiler



Expressing block semantics

How to express the semantics of blocks ?

- Solution 1 : blocks are functions; $G \vdash B(xs) \Downarrow ys$
 - » inputs are the free variables of the block
 - » outputs are the variables defined by the block

Pros:

- » Definition of node semantics is direct

$$\frac{\text{node}(G, f) \doteq B \quad G \vdash B(xs) \Downarrow ys}{G \vdash f(xs) \Downarrow ys}$$

- » Input / Output of blocks can be manipulated

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- Solution 2 : blocks are constraints; $G, H, bs \vdash B$

Reset - Example and Intuition

```
node expect(a : bool)
returns (o : bool)
let
  o = a or (false fby o);
tel
```

a		F	F	T	F	T	F	...
o		F	F	T	T	T	T	...

```
node abro(a, b, r : bool)
returns (o : bool)
let
  reset
    o = expect(a) and expect(b);
  every r
tel
```

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a		F	T	F	F	F	...
b		F	F	F	T	F	...
r		F	F	F	F	F	...
o		F	F	F	T	T	...

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b		T	F	...
r		T	F	...
o		F	F	...

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b	F	F	F	T	F	T	F	F	F	T	...
r	F	F	F	F	F	T	F	T	F	F	...
o	F	F	F	T	T	F	F	F	F	T	...

Reset - stream semantics

$$\text{mask}_{T.rs}^0(x \cdot xs) \equiv \text{always-absent}$$

$$\text{mask}_{F.rs}^0(x \cdot xs) \equiv x \cdot \text{mask}_{rs}^0 xs$$

$$\text{mask}_{T.rs}^1(x \cdot xs) \equiv x \cdot \text{mask}_{rs}^0 xs$$

$$\text{mask}_{T.rs}^{k+1}(x \cdot xs) \equiv \langle \rangle \cdot \text{mask}_{rs}^k xs$$

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<i>rs</i>	F	T	F	T	F	F	T	F	...
<i>xs</i>	1	2	3	4	5	6	7	8	...
$\text{mask}_{rs}^2 xs$	$\langle \rangle$	$\langle \rangle$	$\langle \rangle$	4	5	6	$\langle \rangle$	$\langle \rangle$...

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<i>xs</i>	1	2	3	4	5	6	7	8	...
$\text{mask}_{rs}^2 xs$	⟨⟩	⟨⟩	⟨⟩	4	5	6	⟨⟩	⟨⟩	...

$$G, H, bs \vdash e_r \Downarrow [s] \quad \text{bools-of } s \doteq r$$

$$G, H, bs \vdash \mathbf{es} \Downarrow xs$$

$$\forall k, G \vdash f(\text{mask}_r^k xs) \Downarrow \text{mask}_r^k ys$$

$$G, H, bs \vdash (\text{restart } f \text{ every } e_r)(\mathbf{es}) \Downarrow ys$$

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$\text{mask}_{rs}^2 xs$	$\langle \rangle$	$\langle \rangle$	$\langle \rangle$	4	5	6	$\langle \rangle$	$\langle \rangle$...

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$$\frac{}{G, H, bs \vdash (\text{restart } f \text{ every } e_r)(\mathbf{es}) \Downarrow ys}$$

$$G, H, bs \vdash e_r \Downarrow [s] \quad \text{bools-of } s \doteq r$$

$$\forall k, G, \text{mask}_r^k(H, bs) \vdash \text{blks}$$

$$\frac{}{G, H, bs \vdash \text{reset blks every } e_r}$$

$$\frac{\text{fby } xs \ ys \dot{=} \ vs}{\text{fby } (\langle \rangle \cdot xs) \ (\langle \rangle \cdot ys) \dot{=} \ \langle \rangle \cdot vs}$$

$$\frac{\text{fby}_1 \ y \ xs \ ys \dot{=} \ vs}{\text{fby } (\langle x \rangle \cdot xs) \ (\langle y \rangle \cdot ys) \dot{=} \ \langle x \rangle \cdot vs}$$

$$\frac{\text{fby}_1 \ v \ xs \ ys \dot{=} \ vs}{\text{fby}_1 \ v \ (\langle \rangle \cdot xs) \ (\langle \rangle \cdot ys) \dot{=} \ \langle \rangle \cdot vs}$$

$$\frac{\text{fby}_1 \ y \ xs \ ys \dot{=} \ vs}{\text{fby}_1 \ v \ (\langle x \rangle \cdot xs) \ (\langle y \rangle \cdot ys) \dot{=} \ \langle v \rangle \cdot vs}$$

$$\frac{G, H \vdash e_0 \Downarrow xs \quad G, H \vdash e_1 \Downarrow ys \quad \text{fby } xs \ ys \dot{=} \ vs}{G, H \vdash e_0 \text{ fby } e_1 \Downarrow vs}$$

Lustre fby operator semantics - With reset signal

$$\frac{\text{fby } v \text{ } xs \text{ } ys \text{ } rs \doteq vs}{\text{fby } v \text{ } (\langle \rangle \cdot xs) \text{ } (\langle \rangle \cdot ys) \text{ } (F \cdot rs) \doteq \langle \rangle \cdot vs}$$

$$\frac{\text{fby } \langle y \rangle \text{ } xs \text{ } ys \text{ } rs \doteq vs}{\text{fby } \langle \rangle \text{ } (\langle x \rangle \cdot xs) \text{ } (\langle y \rangle \cdot ys) \text{ } (F \cdot rs) \doteq \langle x \rangle \cdot vs}$$

$$\frac{\text{fby } \langle y \rangle \text{ } xs \text{ } ys \text{ } rs \doteq vs}{\text{fby } \langle v \rangle \text{ } (\langle x \rangle \cdot xs) \text{ } (\langle y \rangle \cdot ys) \text{ } (F \cdot rs) \doteq \langle v \rangle \cdot vs}$$

$$\frac{\text{fby } \langle \rangle \text{ } xs \text{ } ys \text{ } rs \doteq vs}{\text{fby } v \text{ } (\langle \rangle \cdot xs) \text{ } (\langle \rangle \cdot ys) \text{ } (T \cdot rs) \doteq \langle \rangle \cdot vs}$$

$$\frac{\text{fby } \langle y \rangle \text{ } xs \text{ } ys \text{ } rs \doteq vs}{\text{fby } (\langle x \rangle \cdot xs) \text{ } (\langle y \rangle \cdot ys) \text{ } (T \cdot rs) \doteq \langle x \rangle \cdot vs}$$

$$\frac{G, H \vdash e_0 \Downarrow xs \quad G, H \vdash e_1 \Downarrow ys \quad G, H \vdash e_r \Downarrow [s] \quad \text{bools-of } s \doteq r}{\text{fby } \langle \rangle \text{ } xs \text{ } ys \text{ } r \doteq vs}$$

$$G, H \vdash (\text{reset } e_0 \text{ fby } e_1 \text{ every } e_r) \Downarrow vs$$

CoFixpoint sfby v xs :=

```

match str with
| <v'> · xs' ⇒ <v> · (sfby v' xs')
| <> · xs' ⇒ <> · (sfby v xs')
end.

```

CoFixpoint reset1 v0 xs rs doreset :=

```

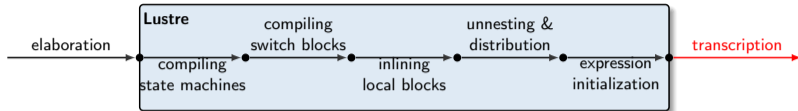
match xs, rs, doreset with
| <> · xs, false · rs, false ⇒ <> · (reset1 v0 xs rs false)
| <> · xs, true · rs, _
| <> · xs, _ · rs, true ⇒ <> · (reset1 v0 xs rs true)
| <x> · xs, false · rs, false ⇒ <x> · (reset1 v0 xs rs false)
| <x> · xs, true · rs, _
| <x> · xs, _ · rs, true ⇒ <v0> · (reset1 v0 xs rs false)
end.

```

Definition reset v0 xs rs := reset1 v0 xs rs false.

$$\frac{
 G, H, bs \vdash e_1 \Downarrow xs \quad G, H, bs \vdash e_r \Downarrow [s] \quad \text{bools-of } s \doteq r
 }{
 G, H, bs \vdash x = (\text{reset } c_0 \text{ fby } e_1 \text{ every } e_r)
 }$$

Reset - Compilation



```
node abro(a, b, r : bool) returns (o : bool)
```

```
var ea, eb, peb : bool;
```

```
let
```

```
  reset
```

```
    ea = expect(a);
```

```
    peb = false fby eb;
```

```
    eb = b or peb;
```

```
    o = ea and eb;
```

```
  every r
```

```
tel
```

```
node abro (a, b, r : bool) returns (o : bool)
```

```
var ea, eb, peb : bool;
```

```
let
```

```
  ea = (restart expect every r)(a);
```

```
  peb = reset (false fby eb) every r;
```

```
  eb = b or peb;
```

```
  o = ea and eb;
```


```
tel
```

Local Blocks - Shadowing rules

```
node f(b : bool; x : int when b) returns (z : int)
let
  var b : bool;
  let
    z = merge b (true -> x) (false -> 0);
    b = true fby false;
  tel
tel
```

Local Blocks - Shadowing rules

```
node f(b : bool; x : int when b) returns (z : int)
let
  var b : bool;
  let
    z = merge b (true -> x) (false -> 0);
    b = true fby false;
  tel
tel
```



Local Blocks - Shadowing rules

```
node f(b : bool; x : int when b) returns (z : int)
let
  var b : bool;
  let
    z = merge b (true -> x) (false -> 0);
    b = true fby false;
  tel
tel
```

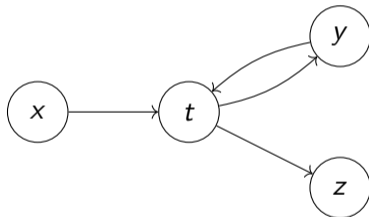

Local Blocks - Shadowing rules

```
node f(b : bool; x : int when b) returns (z : int)
let
  var b : bool;
  let
    z = merge b (true -> x) (false -> 0);
    b = true fby false;
  tel
tel
```

$$\frac{\text{NoDup } xs \quad \forall x, x \in xs \Rightarrow x \notin \Gamma \quad (\Gamma \cup xs) \vdash_{NDL} B}{\Gamma \vdash_{NDL} \text{var } xs \text{ let } B \text{ tel}}$$
$$\frac{\text{NoDup } (n.in \cup n.out) \quad (n.in \cup n.out) \vdash_{NDL} n.blk}{\vdash_{NDL} n}$$

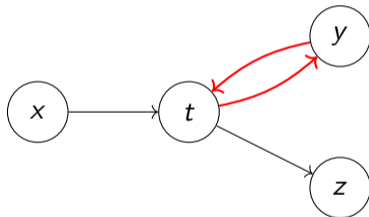
Local Blocks - Causality analysis

```
node f(x : int) returns (z : bool)
var y : int;
let
  var t : int;
  let t = x fby (t + 1);
      y = t;
tel;
var t : int;
let t = y + 1;
    z = t > 0;
tel
tel
```



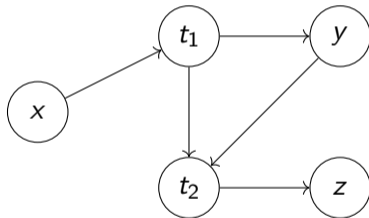
Local Blocks - Causality analysis

```
node f(x : int) returns (z : bool)
var y : int;
let
  var t : int;
  let t = x fby (t + 1);
  y = t;
tel;
var t : int;
let t = y + 1;
  z = t > 0;
tel
tel
```



Local Blocks - Causality analysis

```
node f(x(x1) : int) returns (z(z1) : bool)
var y(y1) : int;
let
  var t(t1) : int;
  let t = x fby (t + 1);
      y = t;
  tel;
  var t(t2) : int;
  let t = y + 1;
      z = t > 0;
  tel
tel
```



$$\frac{G, H', bs \vdash B}{G, H, bs \vdash \text{var } xs \text{ let } B \text{ tel}}$$

R? H H'

$$\frac{G, H', bs \vdash B \quad H \subseteq H'}{G, H, bs \vdash \text{var } xs \text{ let } B \text{ tel}}$$

$$\frac{G, H', bs \vdash B \quad \forall x \, vs, H(x) = vs \Rightarrow H'(x) = vs}{G, H, bs \vdash \text{var } xs \text{ let } B \text{ tel}}$$

Local Blocks - Semantic rules

$$\frac{G, H', bs \vdash B \quad \forall x \text{ vs}, H(x) = \text{vs} \Rightarrow H'(x) = \text{vs}}{G, H, bs \vdash \text{var } xs \text{ let } B \text{ tel}}$$

```
node f(i : int) returns (o : int)
{
  var z : int;
  let
  {
    var x : int;
    let
    {
      x = 1;
      z = x;
    }
    tel
  }
  var t : int;
  let
  {
    t = z;
    o = t;
  }
  tel
}
tel
```

$z \notin H$

$H_1(x) = 1 \cdot 1 \cdot 1 \cdot \dots$
 $H_1(z) = 1 \cdot 1 \cdot 1 \cdot \dots$

$H_2(z) = ?$

Local Blocks - Semantic rules

$$\frac{G, H', bs \vdash B \quad \forall x \, vs, H'(x) = vs \Rightarrow H(x) = vs}{G, H, bs \vdash \text{var } xs \text{ let } B \text{ tel}}$$

$H(z) = 1 \cdot 1 \cdot 1 \cdot \dots$

```
node f(i : int) returns (o : int)
```

```
var z : int;
```

```
let
```

```
var x : int;
```

```
let
```

```
x = 1;
```

```
z = x;
```

```
tel
```

```
var t : int;
```

```
let
```

```
t = z;
```

```
o = t;
```

```
tel
```

```
tel
```

$H_1(x) = 1 \cdot 1 \cdot 1 \cdot \dots$

$H_1(z) = 1 \cdot 1 \cdot 1 \cdot \dots$

$z \in H_2$, therefore

$H_2(z) = 1 \cdot 1 \cdot 1 \cdot \dots$

Local Blocks - Semantic rules

$$\frac{G, H', bs \vdash B \quad \forall x \, vs, H'(x) = vs \Rightarrow H(x) = vs}{G, H, bs \vdash \text{var } xs \text{ let } B \text{ tel}}$$

```
node f(i : int) returns (o : int)
var z : int;
let
  var x : int;
  let
    x = 1;
    z = x;
  tel
  var x : int;
  let
    x = 2;
    o = x;
  tel
tel
```

$H(x) = ?$

$H_1(x) = 1 \cdot 1 \cdot 1 \cdot \dots$
 $H_1(z) = 1 \cdot 1 \cdot 1 \cdot \dots$

$H_2(x) = 2 \cdot 2 \cdot 2 \cdot \dots$

Local Blocks - Semantic rules

$$\frac{G, H', bs \vdash B \quad \forall x \text{ vs}, x \notin xs \Rightarrow H'(x) = vs \Rightarrow H(x) = vs}{G, H, bs \vdash \text{var } xs \text{ let } B \text{ tel}}$$

```
node f(i : int) returns (o : int)
var z : int;
let
  var x : int;
  let
    x = 1;
    z = x;
  tel
  var x : int;
  let
    x = 2;
    o = x;
  tel
tel
```

$x \notin H$

$H_1(x) = 1 \cdot 1 \cdot 1 \cdot \dots$
 $H_1(z) = 1 \cdot 1 \cdot 1 \cdot \dots$

$H_2(x) = 2 \cdot 2 \cdot 2 \cdot \dots$

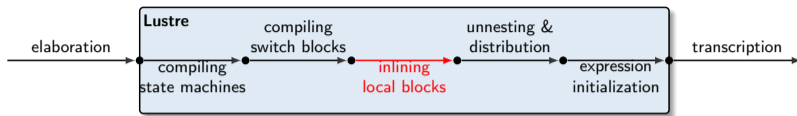
$$\frac{G, H', bs \vdash B \quad \forall x \, vs, x \notin xs \Rightarrow H'(x) = vs \Rightarrow H(x) = vs}{G, H, bs \vdash \text{var } xs \text{ let } B \text{ tel}}$$

$$\frac{\begin{array}{l} \text{node}(G, f) \doteq n \\ H(n.\text{in}) = xs \quad H(n.\text{out}) = ys \\ G, H, (\text{base-of } xs) \vdash n.\text{blk} \end{array}}{G \vdash f(xs) \Downarrow ys}$$

```

node f(i : int) returns (o : int)
var z : int;
let
  var x : int;
  let
    x = 1;
    z = x;
  tel
  var x : int;
  let
    x = 2;
    o = x;
  tel
tel
    
```

Local Blocks - Compilation



```
node f(x : int) returns (z : bool)
```

```
var y : int;
```

```
let
```

```
var t : int;
```

```
let t = x fby (t + 1);
```

```
    y = t;
```

```
tel;
```

```
var t : int;
```

```
let t = y + 1;
```

```
    z = t > 0;
```

```
tel
```

```
tel
```

```
node f(x : int) returns (z : bool)
```

```
var y : int; local$t$2 : int; local$t$1 : int;
```

```
let
```

```
    local$t$1 = x fby (local$t$1 + 1);
```

```
    y = local$t$1;
```

```
    local$t$2 = y + 1;
```

```
    z = local$t$2 > 0
```

```
tel
```

Switch - Stateful example

```
type modes = Up | Down
```

```
node two(m : modes) returns (o : int)
```

```
let
```

```
  switch m
```

```
  | Up -> o = 1 fby (o + 1)
```

```
  | Down -> o = 0
```

```
end
```

```
tel
```



Switch - Stateful example

```
type modes = Up | Down
```

```
node two(m : modes) returns (o : int)
```

```
let
```

```
  switch m
```

```
  | Up -> o = 1 fby (o + 1)
```

```
  | Down -> o = 0
```

```
end
```

```
tel
```

x	U	U	U	U	U	U	...
y	1	2	3	4	5	6	...

base	T	T	T	T	T	T	...
m	U	U	U	U	U	U	...
o	1	2	3	4	5	6	...

Switch - Stateful example

```
type modes = Up | Down
```

```
node two(m : modes) returns (o : int)
```

```
let
```

```
  switch m
```

```
  | Up -> o = 1 fby (o + 1)
```

```
  | Down -> o = 0
```

```
end
```

```
tel
```

x	U	U	U	U	U	U	...
y	1	2	3	4	5	6	...

m	D	D	D	D	...
o	0	0	0	0	...

base		T	T		T	T	...
m		D	D		D	D	...
o		0	0		0	0	...

Switch - Stateful example

```
type modes = Up | Down
```

```
node two(m : modes) returns (o : int)
```

```
let
```

```
  switch m
```

```
  | Up -> o = 1 fby (o + 1)
```

```
  | Down -> o = 0
```

```
end
```

```
tel
```

x	U	U	U	U	U	U	...
y	1	2	3	4	5	6	...

m	D	D	D	D	...
o	0	0	0	0	...

base	T	T	T	T	T	T	T	T	T	T	...
m	U	U	U	D	D	U	U	D	D	U	...
o	1	2	3	0	0	4	5	0	0	6	...

Switch - Semantic rules

$$\text{filter}_{\langle C \rangle . cs}^C (v \cdot vs) \equiv v \cdot \text{filter}_{cs}^C vs$$

$$\text{filter}_{\langle \rangle . cs}^C (v \cdot vs) \equiv \langle \rangle \cdot \text{filter}_{cs}^C vs$$

$$\text{filter}_{\langle C' \rangle . cs}^C (v \cdot vs) \equiv \langle \rangle \cdot \text{filter}_{cs}^C vs \text{ if } C' \neq C$$

$$G, H, bs \vdash e \Downarrow [cs] \quad G, \text{filter}_{cs}^{C_i}(H, bs) \vdash B_i$$

$$G, H, bs \vdash \text{switch } e (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)$$

Switch - Semantic rules

$$\text{filter}_{\langle C \rangle . cs}^C (v \cdot vs) \equiv v \cdot \text{filter}_{cs}^C vs$$

$$\text{filter}_{\langle \rangle . cs}^C (v \cdot vs) \equiv \langle \rangle \cdot \text{filter}_{cs}^C vs$$

$$\text{filter}_{\langle C' \rangle . cs}^C (v \cdot vs) \equiv \langle \rangle \cdot \text{filter}_{cs}^C vs \text{ if } C' \neq C$$

```
node f(b : bool; c : bool when b)
```

```
returns (z : int when b)
```

```
let switch c
```

```
  | true -> z = 1
```

```
  | false -> z = 0
```

```
end
```

```
tel
```

b		T	T	F	T	...
c		T	F	$\langle \rangle$	F	...
z		1	0	?	0	...

$$G, H, bs \vdash e \Downarrow [cs] \quad G, \text{filter}_{cs}^{C_i}(H, bs) \vdash B_i$$

$$G, H, bs \vdash \text{switch } e (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)$$

Switch - Semantic rules

$$\text{filter}_{\langle C \rangle, cs}^C (v \cdot vs) \equiv v \cdot \text{filter}_{cs}^C vs$$

$$\text{filter}_{\langle \rangle, cs}^C (v \cdot vs) \equiv \langle \rangle \cdot \text{filter}_{cs}^C vs$$

$$\text{filter}_{\langle C' \rangle, cs}^C (v \cdot vs) \equiv \langle \rangle \cdot \text{filter}_{cs}^C vs \text{ if } C' \neq C$$

$$\frac{\text{slower } xs \ bs}{\text{slower } (\langle \rangle \cdot xs) \ (F \cdot bs)} \quad \frac{\text{slower } xs \ bs}{\text{slower } (v \cdot xs) \ (T \cdot bs)}$$

$$G, H, bs \vdash e \Downarrow [cs] \quad G, \text{filter}_{cs}^{C_i}(H, bs) \vdash B_i$$

$$G, H, bs \vdash \text{switch } e \ (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)$$

```
node f(b : bool; c : bool when b)
```

```
returns (z : int when b)
```

```
let switch c
```

```
  | true -> z = 1
```

```
  | false -> z = 0
```

```
end
```

```
tel
```

b	T	T	F	T	...
c	T	F	$\langle \rangle$	F	...
z	1	0	$\langle \rangle$	0	...

Switch - Semantic rules

$$\text{filter}_{\langle C \rangle, cs}^C (v \cdot vs) \equiv v \cdot \text{filter}_{cs}^C vs$$

$$\text{filter}_{\langle \rangle, cs}^C (v \cdot vs) \equiv \langle \rangle \cdot \text{filter}_{cs}^C vs$$

$$\text{filter}_{\langle C' \rangle, cs}^C (v \cdot vs) \equiv \langle \rangle \cdot \text{filter}_{cs}^C vs \text{ if } C' \neq C$$

$$\frac{\text{slower } xs \ bs}{\text{slower } (\langle \rangle \cdot xs) \ (F \cdot bs)} \quad \frac{\text{slower } xs \ bs}{\text{slower } (v \cdot xs) \ (T \cdot bs)}$$

```
node f(b : bool; c : bool when b)
```

```
returns (z : int when b)
```

```
let switch c
```

```
  | true -> z = 1
```

```
  | false -> z = 0
```

```
end
```

```
tel
```

b	T	T	F	T	...
c	T	F	$\langle \rangle$	F	...
z	1	0		0	...

$$\frac{G, H, bs \vdash e \Downarrow [cs] \quad G, \text{filter}_{cs}^{C_i}(H, bs) \vdash B_i \quad \forall x, x \in VD(\text{switch } e (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)) \Rightarrow \text{slower } H(x) \text{ (abstract_clock } cs)}{G, H, bs \vdash \text{switch } e (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)}$$

Switch - Clocking rules

[Colaço, Pagano, and Pouzet (2005): A
Conservative Extension of Synchronous
Data-flow with State Machines]

The clock calculus must be extended such that translated program can be accepted by the basic clock calculus and can thus be safely compiled. Remember that we have introduced the notation $CO_n D C(c)$ to say that every free variable in a block is observed on the local clock defined by the block. We now define $H on_{ck} C(c)$ to apply on clocking environment in order to simulate this process during the clock calculus. Consider for example a **match/with** statement which is itself executed on some clock ck . When entering in a branch, a free variable x with defined clock ck will be read on the sub-clock ck on $C(c)$ of ck .

$(H on_{ck} C(c))(x) = H(x) on C(c)$ provided $H(x) = ck$

For example, if $H = [\alpha/x_1, \alpha/x_2]$ then $H on_{\alpha} (C(c) : \alpha)$ is an environment H' such that the clock information associated to x_1 in H' is $\alpha on C(c)$. As a consequence, if a free variable x with defined clock ck on $C'(c')$ instead of ck , then

Switch - Clocking rules

[Colaço, Pagano, and Pouzet (2005): A Conservative Extension of Synchronous Data-flow with State Machines]

The clock calculus must be extended such that translated program can be accepted by the basic clock calculus and can thus be safely compiled. Remember that we have introduced the notation $CO_n D C(c)$ to say that every free variable in a block is observed on the local clock c defined by the block. We now define $H on_{ck} C(c)$ to apply to a block $C(c)$ in order to simulate this program. Consider for example a match executed on some clock ck . free variable x with defined sub-clock ck on $C(c)$ of ck .

$$(H on_{ck} C(c))(x) = H(x)$$

For example, if $H = [\alpha/x]$ an environment H' such that α is related to x_1 in H' is α on $C(c)$.

Figure 7: The Extended Clock System

Switch - Clocking rules

$(H \text{ on}_{ck} C(c))(x) = H(x) \text{ on } C(c)$ **provided** $H(x) = ck$

[Colaço, Pagano, and Pouzet (2005): A Conservative Extension of Synchronous Data-flow with State Machines]

$$\frac{H \vdash e_c : ck \quad m \notin N(H) \quad H \text{ on}_{ck} C_i(m) \vdash B_i}{H \vdash \text{switch } e_c (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)}$$

$$\frac{H \vdash e_c : ck \quad H' \vdash B_i \quad \forall x, H'(x) = . \text{ provided } H(x) = ck}{H \vdash \text{switch } e_c (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)}$$

Switch - Clocking rules

$(H \text{ on}_{ck} C(c))(x) = H(x) \text{ on } C(c)$ **provided** $H(x) = ck$

[Colaço, Pagano, and Pouzet (2005): A Conservative Extension of Synchronous Data-flow with State Machines]

$$\frac{H \vdash e_c : ck \quad m \notin N(H) \quad H \text{ on}_{ck} C_i(m) \vdash B_i}{H \vdash \text{switch } e_c (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)}$$

introduces a skolem variable

$$\frac{H \vdash e_c : ck \quad H' \vdash B_i \quad \forall x, H'(x) = . \text{ provided } H(x) = ck}{H \vdash \text{switch } e_c (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)}$$

Switch - Clocking rules

$(H \text{ on}_{ck} C(c))(x) = H(x) \text{ on } C(c)$ **provided** $H(x) = ck$

[Colaço, Pagano, and Pouzet (2005): A Conservative Extension of Synchronous Data-flow with State Machines]

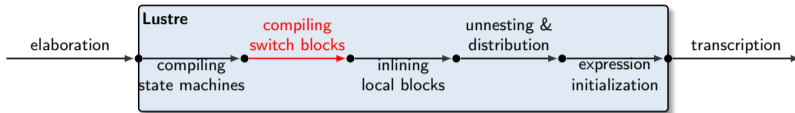
$H \vdash e_c : ck$ $m \notin N(H)$ $H \text{ on}_{ck} C_i(m) \vdash B_i$
introduces a skolem variable

 $H \vdash \text{switch } e_c (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)$

$H \vdash e_c : ck$ $H' \vdash B_i$ $\forall x, H'(x) = .$ **provided** $H(x) = ck$
only one base clock

 $H \vdash \text{switch } e_c (C_1 \rightarrow B_1) \dots (C_n \rightarrow B_n)$

Switch - Compilation



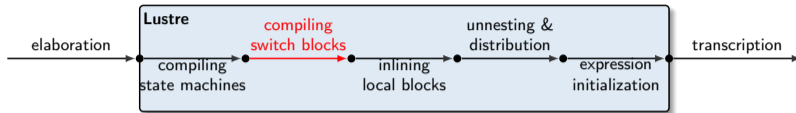
```
type modes = Up | Down
```

```
node two(m : modes) returns (o : int)
let
  switch m
  | Up -> o = 1 fby (o + 1)
  | Down -> o = 0
end
tel
```

```
type modes = Up | Down
```

```
node two (m : modes) returns (o : int)
var swi$m$1 : modes when (m=Up); swi$o$2 : int when (m=Up);
  swi$m$3 : modes when (m=Up); swi$o$4 : int when (m=Down);
let
  let
    o = merge m (Up -> swi$o$2) (Down -> swi$o$4);
    swi$o$2 = (1 when (m=Up)) fby (swi$o$2 + (1 when (m=Down)));
    swi$m$1 = m when (m=Up);
    swi$o$4 = 0 when (m=Down);
    swi$m$3 = m when (m=Up);
  tel
tel
```

Switch - Compilation



```
type modes = Up | Down
```

```
node two(m : modes) returns (o : int)
let
  switch m
  | Up -> o = 1 fby (o + 1)
  | Down -> o = 0
end
tel
```

```
type modes = Up | Down
```

```
node two (m : modes) returns (o : int)
var swi$m$1 : modes when (m=Up); swi$o$2 : int when (m=Up);
  swi$m$3 : modes when (m=Up); swi$o$4 : int when (m=Down);
let
  let
    o = merge m (Up -> swi$o$2) (Down -> swi$o$4);
    swi$o$2 = (1 when (m=Up)) fby (swi$o$2 + (1 when (m=Down)));
    swi$m$1 = m when (m=Up);
    swi$o$4 = 0 when (m=Down);
    swi$m$3 = m when (m=Up);
  tel
tel
```

Hierarchical State Machines - Example

```
node updown(b : bool) returns (y : int)
```

```
let
```

```
  automaton
```

```
  | U ->
```

```
    y = start fby (y + inc);
```

```
    until y > 1 restart D;
```

```
    until y > 2 restart U
```

```
  | D ->
```

```
    y = start fby (y - inc);
```

```
    until y < -2 resume U
```

```
end
```

```
initially D if false; I otherwise
```

```
tel
```

base	T	T	T	T	T	T	T	T	T	T	T	...
b	F	F	F	F	F	F	F	F	F	F	F	...
y	0	1	2	0	-1	-2	3	0	1	2	0	...

Hierarchical State Machines - Example

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let
```

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```
end
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```

```
tel
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base	T	T	T	T	T	T	T	T	T	T	T	...
b	F	F	F	F	F	F	F	F	F	F	F	...
y	0	1	2	0	-1	-2	3	0	1	2	0	...
state	U	U	U	D	D	D	U	U	U	U	D	...
reset	F	F	F	T	F	F	F	T	F	F	T	...

Hierarchical State Machines semantics can be encoded reactive or coiterative semantics

- [Colaço, Hamon, and Pouzet (2006): Mixing Signals and Modes in Synchronous Data-flow Systems]
- [Caspi and Pouzet (1997): A Co-iterative Characterization of Synchronous Stream Functions]

Hierarchical State Machines semantics can be encoded reactive or coiterative semantics

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It doesn't seem to be possible to mix-and-match these styles with our stream semantics
Instead, we encode a stream of entering transitions

- at each instant, indicates which state is entered, and if it is entered with reset
- transitions can be absent if the state machine is inactive
- only weak transitions (for the moment)

Hierarchical State Machines - Transitions

$\text{const-st } (T \cdot bs) \text{ st} \equiv \langle st \rangle \cdot \text{const-st } bs \text{ st}$

$\text{const-st } (F \cdot bs) \text{ st} \equiv \langle \rangle \cdot \text{const-st } bs \text{ st}$

$$\frac{G, H, bs \vdash e \Downarrow [ys] \quad \text{bools-of } ys \doteq bs'}{G, H, bs \vdash \text{until } e \text{ resume } C \Downarrow (\text{const-st } bs' (C, F))}$$

$$\frac{G, H, bs \vdash e \Downarrow [ys] \quad \text{bools-of } ys \doteq bs'}{G, H, bs \vdash \text{until } e \text{ restart } C \Downarrow (\text{const-st } bs' (C, T))}$$

Hierarchical State Machines - Transitions

$$\begin{array}{l} \text{const-st } (T \cdot bs) \text{ } st \equiv \langle st \rangle \cdot \text{const-st } bs \text{ } st \\ \text{const-st } (F \cdot bs) \text{ } st \equiv \langle \rangle \cdot \text{const-st } bs \text{ } st \end{array} \quad \frac{G, H, bs \vdash e \Downarrow [ys] \quad \text{bools-of } ys \doteq bs'}{G, H, bs \vdash \text{until } e \text{ resume } C \Downarrow (\text{const-st } bs' (C, F))}$$

$$\frac{G, H, bs \vdash e \Downarrow [ys] \quad \text{bools-of } ys \doteq bs'}{G, H, bs \vdash \text{until } e \text{ restart } C \Downarrow (\text{const-st } bs' (C, T))}$$

$$\begin{array}{l} \text{choose-fst } (\langle \rangle \cdot vs_1) \dots (\langle v \rangle \cdot vs_k) \dots (v_n \cdot vs_n) \equiv \langle v \rangle \cdot (\text{choose-fst } vs_1 \dots vs_n) \\ \text{choose-fst } (\langle \rangle \cdot vs_1) \dots (\langle \rangle \cdot vs_n) \equiv \langle \rangle \cdot (\text{choose-fst } vs_1 \dots vs_n) \end{array}$$

$$\frac{G, H, bs \vdash \text{until}_i \Downarrow ts_i}{G, H, bs \vdash (C \rightarrow \text{until}_1 \dots \text{until}_n) \Downarrow \text{choose-fst } ts_1 \dots ts_n (\text{const-st } bs (C, F))}$$

Hierarchical State Machines - Putting it all together

$$\frac{\begin{array}{l} (H_i, bs_i) = \text{filter}_{\pi_1(ts)}^C (H, bs) \quad rs_i = \text{filter}_{\pi_1(ts)}^C \pi_2(ts) \\ \forall k. G, \text{mask}_{rs_i}^k (H_i, bs_i) \vdash B \quad \forall k. G, \text{mask}_{rs_i}^k (H_i, bs_i) \vdash \text{untils} \Downarrow ts' \end{array}}{G, H, bs, ts \vdash (C \rightarrow B; \text{untils}) \Downarrow ts'}$$

Hierarchical State Machines - Putting it all together

$$\frac{\begin{array}{l} (H_i, bs_i) = \text{filter}_{\pi_1(ts)}^C (H, bs) \quad rs_i = \text{filter}_{\pi_1(ts)}^C \pi_2(ts) \\ \forall k. G, \text{mask}_{rs_i}^k (H_i, bs_i) \vdash B \quad \forall k. G, \text{mask}_{rs_i}^k (H_i, bs_i) \vdash \text{untils} \Downarrow ts' \end{array}}{G, H, bs, ts \vdash (C \rightarrow B; \text{untils}) \Downarrow ts'}$$

$$\frac{\begin{array}{l} G, H, bs \vdash \text{autinits} \Downarrow ts_0 \\ G, H, bs, ts \vdash \text{autst}_i \Downarrow ts_i \\ \text{fby } ts_0 \ ts_1 \doteq ts \end{array}}{G, H, bs \vdash \text{automaton } \text{autst}_1 \dots \text{autst}_n \text{ initially } \text{autinits}}$$

Hierarchical State Machines - Putting it all together

$$\frac{(H_i, bs_i) = \text{filter}_{\pi_1(ts)}^C (H, bs) \quad rs_i = \text{filter}_{\pi_1(ts)}^C \pi_2(ts) \quad \forall k. G, \text{mask}_{rs_i}^k(H_i, bs_i) \vdash B \quad \forall k. G, \text{mask}_{rs_i}^k(H_i, bs_i) \vdash \text{untils} \Downarrow ts'}{G, H, bs, ts \vdash (C \rightarrow B; \text{untils}) \Downarrow ts'}$$

$$\frac{\text{constrains-present } xs \ ys}{\text{constrains-present } (\langle x \rangle \cdot xs) (\langle x \rangle \cdot ys)} \quad \frac{\text{constrains-present } xs \ ys}{\text{constrains-present } (\langle \rangle \cdot xs) (y \cdot ys)}$$

$$\frac{G, H, bs \vdash \text{autinits} \Downarrow ts_0 \quad G, H, bs, ts \vdash \text{autst}_i \Downarrow ts_i \quad \text{constrains-present } ts_i \ ts_1 \quad \text{fby } ts_0 \ ts_1 \doteq ts}{G, H, bs \vdash \text{automaton } \text{autst}_1 \dots \text{autst}_n \text{ initially } \text{autinits}}$$

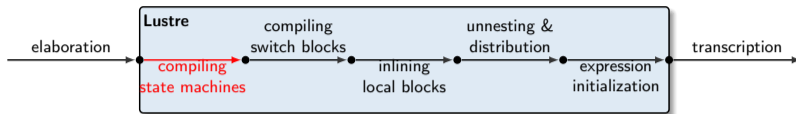
Hierarchical State Machines - Putting it all together

$$\frac{(H_i, bs_i) = \text{filter}_{\pi_1(ts)}^C (H, bs) \quad rs_i = \text{filter}_{\pi_1(ts)}^C \pi_2(ts) \quad \forall k. G, \text{mask}_{rs_i}^k(H_i, bs_i) \vdash B \quad \forall k. G, \text{mask}_{rs_i}^k(H_i, bs_i) \vdash \text{untils} \Downarrow ts'}{G, H, bs, ts \vdash (C \rightarrow B; \text{untils}) \Downarrow ts'}$$

$$\frac{\text{constrains-present } xs \ ys}{\text{constrains-present } (\langle x \rangle \cdot xs) (\langle x \rangle \cdot ys)} \quad \frac{\text{constrains-present } xs \ ys}{\text{constrains-present } (\langle \rangle \cdot xs) (y \cdot ys)}$$

$$\frac{G, H, bs \vdash \text{autinits} \Downarrow ts_0 \quad G, H, bs, ts \vdash \text{autst}_i \Downarrow ts_i \quad \text{constrains-present } ts_i \ ts_1 \quad \text{slower } ts_1 \ bs \quad \text{fby } ts_0 \ ts_1 \doteq ts}{G, H, bs \vdash \text{automaton } \text{autst}_1 \dots \text{autst}_n \text{ initially } \text{autinits}}$$

Hierarchical State Machines - Compilation



```
node updown(b : bool) returns (y : int)
let
  automaton
  | U ->
    y = start fby (y + inc);
    until y > 1 restart D;
    until y > 2 restart U
  | D ->
    y = start fby (y - inc);
    until y < -2 resume U
  end
  initially D if false; I otherwise
tel
```

```
type ty$1 = U | D
```

```
node updown(b : bool) returns (y : int)
var st$1, pst$1 : ty$1; res$1, pres$1 : bool;
let
  st$1 = (if b then D else I) fby pst$1;
  res$1 = false fby pres$1;
  switch st$1
  | U ->
    reset
    y = start fby (y + inc);
    (pst$1, pres$1) = if y > 1 then (D, true) else if y > 2 then (U, true) else (U, false);
    every res$1
  | D ->
    reset
    y = start fby (y - inc);
    (pst$1, pres$1) = if y < -2 then (U, false) else (D, false);
    every res$1
  end
tel
```


Shared variables ?

```
node updown() returns (y : int)
var last x : int = 0;
let y = x;
  automaton
  | Up ->
    x = last x + 1;
    until x > 2 resume Down
  | Down ->
    x = last x - 1;
    until x <= 0 resume Up
  initially Up
tel
```

last x	0	1	2	3	2	1	0	1	...
x, y	1	2	3	2	1	0	1	2	...

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last x	0	1	2	3	2	1	0	1	...
x, y	1	2	3	2	1	0	1	2	...

```
node updown() returns (y : int)
var x, px : int;
let y = x;
  px = 0 fby x;
  automaton
  | Up ->
    x = px + 1;
    until x > 2 resume Down
  | Down ->
    x = px - 1;
    until x <= 0 resume Up
  initially Up
tel
```

What's next ?

What's left to do:

- Dead code optimization
- Specification and compilation of state machines
- Specification and compilation of last expressions

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What I want to explore next:

- Coiterative interpreter in Velus / proof of existence
- Link with the work of Paul Jeanmaire




What's next ?

What's left to do:

- Dead code optimization
- Specification and compilation of state machines
- Specification and compilation of last expressions

What I want to explore next:

- Coiterative interpreter in Velus / proof of existence
- Link with the work of Paul Jeanmaire
- Specifying and adding external (C) nodes in Velus

-  Caspi, P. and M. Pouzet (Oct. 1997). *A Co-iterative Characterization of Synchronous Stream Functions*. Research Report 97-07. Gières, France: VERIMAG.
-  Colaço, J.-L., G. Hamon, and M. Pouzet (Oct. 2006). “Mixing Signals and Modes in Synchronous Data-flow Systems”. In: *Proc. 6th ACM Int. Conf. on Embedded Software (EMSOFT 2006)*. Ed. by S. L. Min and Y. Wang. Seoul, South Korea: ACM Press, pp. 73–82.
-  Colaço, J.-L., B. Pagano, and M. Pouzet (Sept. 2005). “A Conservative Extension of Synchronous Data-flow with State Machines”. In: *Proc. 5th ACM Int. Conf. on Embedded Software (EMSOFT 2005)*. Ed. by W. Wolf. Jersey City, USA: ACM Press, pp. 173–182.